There is no bit within the field for which an entry is evicted when the bit has been at a particular logical state for at least a predetermined time.

The following comments are in regard to other art made of record by the examiner.

U.S. Patent Numbers 4,168,541, 4,322,795, 6,381,676, 6,493,800, 6,532,520, 6,542,966, and U.S. Patent Application Publication US 2003/0070045 do not teach or suggest evicting an entry in a cache when a bit is at a particular logical state after at least a predetermined time after being set to the particular logical state.

This application is considered to be in condition for allowance and such action is earnestly solicited.

Respectfully submitted,

by _A. L. W. field_

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